2112 25W

**PATENT** 

JUL 2 5 2005 BY Applicant:

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Tjia

Examiner:

Huynh, K.

Application No.:

10/040,180

Group Art Unit:

2112

Filed:

October 23, 2001

Docket No.:

SG 010008

(VLSI.412PA)

Title:

Interrupt Generation in a Bus System

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 20, 2005.

OFFICE ACTION RESPONSE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the non-final Office Action dated April 20, 2005, please consider the following remarks.

The claims are reflected in the listing of claims beginning at page 2 and the Remarks begin on page 5.